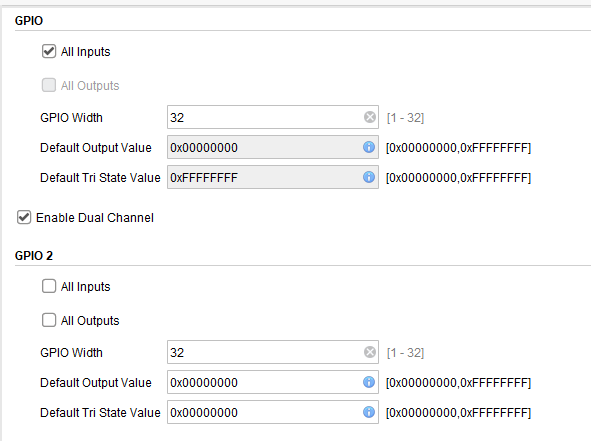


System Clock (125MHz)

Measurementdata 2 Channels  
Channel 1 [15:0]  
Channel 2 [16:31]

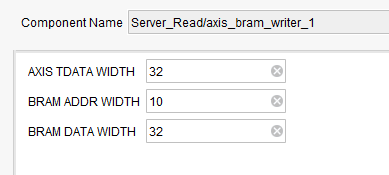
# Axi\_gpio\_0

Setup:  
 

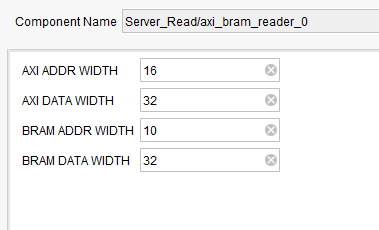
* Axi routing via autoconnect
* Gpio\_io\_i 🡪 measurement data
* Gpio2\_io\_i 🡪 ready of measurement\_control
* Gpio2\_io\_o 🡪 ready of measurement\_control
* Gpio2\_io\_t 🡪slice bit31 and connect to aresetn of measurement\_control

# Axis\_bram\_writer\_1

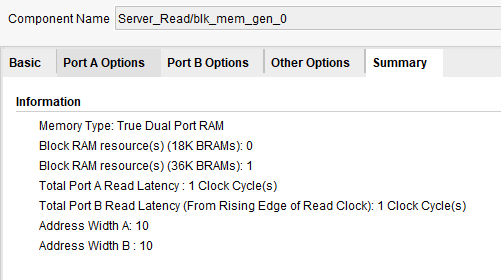
Setup:



# Axi\_bram\_reader\_0

Setup:  


# Blk\_mem\_gen\_0

Setup:  


# Measurement\_control\_0

Setup:  
